

IN THE CLAIMS

1. (Original) A storage control apparatus coupled to a central processing unit and a storage unit to control input/output of data between said central processing unit and said storage unit, comprising:

at least two processors coupled to said central processing unit and said storage unit;

a cache memory unit for temporarily storing data of said storage unit;

a shared memory unit for storing information concerning control of said cache memory unit and said storage unit; and

a selector coupled to each of said at least two processors, said cache memory unit and said shared memory unit through access paths to selectively apply access requests from said at least two processors to said cache memory unit and said shared memory unit.

Claims 2-19 (Canceled)